```vhdl
use Work.ABRO_data_pkg.all;
use Work.ABRO_data_type_pkg.all;

package body ABRO_data_sim_pkg is
    function check_data( check_data_0: in string) return boolean is
    begin
        return true;
    end check_data;

    function text_to_data( text_to_data_0: in string) return data is
        variable buf : data;
    begin
        buf.dataBody := format_substring(text_to_data_0);
        return buf;
    end text_to_data;

    function data_to_text( signal data_to_text_0: in data) return string is
    begin
        return string'(data_to_text_0.dataBody);
    end data_to_text;

    function "="( eq_data_0 : in data; eq_data_1 : in data) return boolean is
    begin
        return string'(eq_data_1.dataBody) = string'(eq_data_0.dataBody);
    end;

    function "/="( neq_data_0 : in data; neq_data_1 : in data) return boolean is
    begin
        return string'(neq_data_1.dataBody) /= string'(neq_data_0.dataBody);
    end;

    function "<"( less_data_0 : in data; less_data_1 : in data) return boolean is
    begin
        return string'(less_data_0.dataBody) < string'(less_data_1.dataBody);
    end;

    function "<="( lesseq_data_0 : in data; lesseq_data_1 : in data) return boolean is
    begin
        return string'(lesseq_data_0.dataBody) <= string'(lesseq_data_1.dataBody);
    end;


end ABRO_data_sim_pkg;
```