

Bertrand B. Blanc

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Technology Facilitator in Data Management

Proven expert driven by product Time-to-Market & Quality improvement with wide SoC Register Management, IP assembly & connectivity, IP/sub-systems packaging for seamless integration, EDA 3rd party vendor roadmap driving. Envision future integration platform from ESL to RTL thru IP-XACT communication backbone.

- Outstanding commitment to bring the absolute highest level of service and best practices to all clients.
- Tackles every project with enthusiasm and a drive to find the best solution for the client and for the technical environment by taking a consultative approach to discovering needs and solutions.

CORE COMPETENCIES

**Process Improvement • Technology Deployment • Software Development
Technology Evaluation • System Design • Architecture • Process Reengineering
Infrastructure Design • Procedure Development • Quality Assurance**

TECHNICAL SUMMARY

SW languages	: C/C++, lex/yacc, scripts shell (CSH/SH)
HW languages	: VHDL, Verilog, Esterel, IP-XACT
Web Technologies	: HTML, XML, XSLT, CSS, PHP, JavaScript
Operating Systems	: Linux, MS-Windows, UNIX Solaris
Database	: Oracle, SQL, Salesforce, MySQL
Network	: OSI, TCP/IP, sockets, protocols, administration notions
Algorithmic	: Proof, Space & Time Complexity, Scalability, Logics
CRM	: Salesforce
File Management	: ClearCase, CVS, BugZilla
Edition	: MS-Office (Word, Excel, PowerPoint, Outlook), Latex
Methodologies	: UML, design pattern, software life-cycles, OOD/OOP

PROFESSIONAL EXPERIENCE

3B CONSULTANCY - Marseille, France & Los Gatos, CA
President & Principal Engineer

10/2005 – *Present*

Successfully led this single-member member-managed LLC to provide affordable solutions and valuable skills to customers on a time critical basis to give them the resources they need to move internal projects forward without the time lag in hiring direct staff resources to correct Information Technology issues. Managed EDA software research & development and software project audits. Led EDA software projects with existing team members. Followed up with customers and 3rd party vendors to ensure customer satisfaction.

Accomplishments:

- Main customer was Texas Instruments for last past 3 years, involved in SoC Methodology department, and in OMAP Verification department
- Co-authored the white paper "*Endianness or where is Byte 0*" downloaded 12k+ times

TEXAS INSTRUMENTS - VILLENEUVE-LOUBET, FRANCE

6/2003 – 9/2008

Consultant - SoC Methodologies dpt thru 3B Consultancy (2005-2008)

Technical Account Manager - OMAP Verification dpt thru Beach Solutions (2004-2005)

Engineer Sub-Contractor - Nokia Business Unit thru Esterel Technologies (2003-2004)

Defined and drove next generation WW Methodologies for SoC Register Management, SoC Integration & Connectivity, IP Packaging and Flows over IP-XACT backbone. Technical Lead of 3rd party vendors for Project Audit & Consultancy Services to meet milestones and customer satisfaction. Managed SoC Register Management, 3rd party dedicated tool creation. Led SoC Connectivity & Assembly, driving 3rd party dedicated tool leading to dramatically speeding up SoC Integration thru automation. Spearheaded IP & sub-system packaging to develop 3rd party tool to expedite SoC assembly at upper level.

Accomplishments:

- Diagnosed struggles faced in execution of R&D process in order to lead tactical and strategic solutions to resume production.
- Represented customer concerns to The SPIRIT Consortium aiming at standardizing IP-XACT, a format enabling communication from ESL to RTL in semiconductor flows.
- Defined BITWISE features (DUOLOG Technologies product) fitting Texas Instruments expectations
- Defined PANDORA & NAXOS (MAGILLEM DS derivative Products) fitting Texas Instruments expectations

Beach Solutions - (FR, UK, US-CA)

9/2004 – 7/2005

Technical Account Manager

Led full life cycle business development for pre-sales, post-sales and world-wide accounts follow-up (SoC Methodologies). Hired by Beach Solutions to support the Texas Instruments account. Generated feedback reports to improve QA and tool features to meet customers' needs. Collaborate with 3rd parties to ensure compliance to SPIRIT and OCP2 standards.

Accomplishments:

- Defined EASI Studio features requested in OMAP execution teams by Texas Instruments
- Supported in-house toolset for 150 world-wide end-users

Texas Instruments - VILLENEUVE-LOUBET, FRANCE

3/2003 – 8/2004

Consultant (Esterel Technologies)

Tackled a six month project to define methodologies with Esterel language to design and validate an IPs from OMAP platform. Supported and defined proposals, enhancements and improvements for register capture.

Accomplishments:

- Proof of Concept was a success leading to have our paper awarded at SAME'04 workshop: "*Multiclock Design and Synthesis with Esterel*". Co-authored with G. Berry Emeritus member of the French Academy of Sciences.
- Register enhancements features definitions and proof of concept implementation, led to the foundations of a leading-edge new register description language. This will be used 5 years after by MRV tool from MAGILLEM Design Services as their native Textual Input Language.

Institut National de Recherche en Informatique et Automatique - SOPHIA-ANTIPOLIS, FRANCE 1/2002 – 9/2002

Research Internship – Master's Thesis

Defined specifications and implemented a Pure Esterel to behavioral VHDL compiler. This allowed an interface between Esterel, a synchronous language devoted to specify, implement, prove and synthesize real-time embedded systems, and VHDL, a hardware description language which aims to simulate hardware systems. Results presented at the workshop SYNCHRON'02.

Esterel Technologies (R&D dpt.) - VILLENEUVE-LOUBET, FRANCE
MsSE Internship & Part-time Employment

2001 – 2002

Defined specifications and implemented a Hardware Description Language test-bench generator for Esterel scenarios, creating an equivalent scenario into various targeted languages: VHDL, Verilog and C.

Centre National de Recherche Scientifique - MARSEILLES, FRANCE
MsCS Internship

1999 – 2000

Defined specifications and implemented a language for real-time experiments pilot scheme at this Electronics and Computer Science Laboratory.

EDUCATION

Master of Science in Software Engineering

Polytech'Sophia (formerly Ecole Supérieure en Sciences de l'Informatique - ESSI), France

Master of Philosophy in Computer Sciences

University of Nice-Sophia-Antipolis, France

Master of Science in Computer Sciences

University of Provence, Marseilles, France

Bachelor of Science in Computer Sciences

University of Provence, Marseilles, France

Undergraduate Degree in Mathematics

University of Provence, Marseilles, France

French Baccalauréat

Lycée Saint Joseph Les Maristes, Marseilles, France